

<b>Notice of References Cited</b>	Application/Control No. 10/840,076		Applicant(s)/Patent Under Reexamination HARRIS ET AL.	
	Examiner Glenn A. Auve		Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2005/0226168 A1	10-2005	Busch et al.	370/254
*	B	US-2005/0099970 A1	05-2005	Halliday, David J.	370/321
*	C	US-2004/0078506 A1	04-2004	Wong et al.	710/301
*	D	US-2005/0078706 A1	04-2005	Spencer, David M.	370/469
*	E	US-2004/0233652 A1	11-2004	Sandy et al.	361/788
*	F	US-2005/0251606 A1	11-2005	Harris et al.	710/301
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	VXS VMEbus Switched Serial Standard, VITA 41.0-200x, Revision 1.6, VITA Standards Organization, 19 January 2003.
	V	"VXS: VMEbus Switched Serial, A novel and emerging architecture for embedded computing", Dr. Jeffrey M. Harris, Motorola Computer Group (presentation slides), 2001.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.